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a plurality of conduction sections each defining at least part of an external terminal and substantially delineating fabrication alignment, the conduction sections being electrically connected to the leads.

Sub D21 7. (Amended) A semiconductor apparatus, comprising:

a semiconductor device having a plurality of electrodes;

a substrate main body;

C2 a plurality of leads formed on the substrate main body, the plurality of leads radially extending from a peripheral area toward a central area of the substrate main body; and

a plurality of conduction sections formed on the substrate main body and substantially delineating fabrication alignment, the conduction sections being electrically connected to the leads, and one of the conduction sections defining an external terminal.

REMARKS

Claims 1-4, 6-12 and 19-25 are pending. By this Amendment, claims 1 and 7 are amended merely for clarity.

The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. 1.121(c)(ii)).

Specifically, claims 1 and 7 are amended to recite a plurality of conduction sections each defining at least part of an external terminal and substantially delineating fabrication alignment.

I. THE CLAIMS DEFINE ALLOWABLE SUBJECT MATTER

The Office Action rejects claims 1-4 and 6-12 under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,293,067 to Thompson et al. (hereinafter Thompson). The rejection is respectfully traversed.

The Examiner has asserted that Thompson teaches a plurality of leads radially extending from a peripheral area toward a central area of the substrate main body. See, for example, Fig. 1. These assertions are respectfully traversed.

Specifically, and assuming *arguendo* that Thompson teaches a plurality of leads radially extending away from a peripheral area toward a central area of the substrate main body, Thompson does not disclose or suggest a plurality of conduction sections each defining at least part of an external terminal and substantially delineating fabrication alignment, as recited in independent claim 1 and similarly recited in independent claim 7.

Instead, Thompson only shows a specific and particular lead pattern comprising a plurality of leads 20, and through-holes 22. See, for example, Fig. 1.

Therefore, Applicant asserts that Thompson does not disclose or suggest at least a plurality of conduction sections each defining at least part of an external terminal and substantially delineating fabrication alignment, as recited in independent claim 1 and similarly in independent claim 7. Furthermore, Thompson does not even suggest any fabrication delineation upon a substrate, which would assist in improved efficiency during device fabrication.

Therefore, independent claims 1 and 7 define patentable subject matter. Claims 2-4, 6 and 8-12, which depend from independent claim 1, and independent claim 7 respectively, are likewise distinguishable over Thompson for at least the reasons discussed, as well as for the additional features they recite, and also define patentable subject matter.

Accordingly, Applicant requests that the rejection of claims 1-4 and 6-12 be withdrawn.

II. CONCLUSION

In view of the foregoing remarks, Applicant respectfully submits that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-4, 6-12 and 19-25 are earnestly solicited.

Should the Examiner believe anything further is desirable in order to place the application in even better condition for allowance, the Examiner is invited to contact Applicant's undersigned representative at the telephone number listed below.

Respectfully submitted,



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JAO:WDT/fpw

Attachment:
Appendix

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